

Flyback Transformers For National Semiconductor LM5070 PoE Interface – 3 Watt



- Flyback transformers for 3 W PoE applications
- · Designed to operate in continuous mode at 250 kHz with an input of 36 - 72 Vdc
- · 1500 Vrms isolation from primary and bias to secondary

Core material Ferrite

Terminations RoHS tin-silver over tin over nickel over phos bronze. Other terminations available at additional cost. Weight 2.05 g

Ambient temperature -40°C to +85°C

Storage temperature Component: -40°C to +85°C. Packaging: -40°C to +80°C

Resistance to soldering heat Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles Moisture Sensitivity Level (MSL) 1 (unlimited floor life at <30°C / 85% relative humidity)

Failures in Time (FIT) / Mean Time Between Failures (MTBF) 38 per billion hours / 26,315,789 hours, calculated per Telcordia SR-332 Packaging 350 per 13" reel Plastic tape: 32 mm wide, 0.42 mm thick, 20 mm pocket spacing, 9.16 mm pocket depth

PCB washing Only pure water or alcohol recommended

Part	Inductance at 0 A ²	Inductance at Ipk ³	DCR max (Ohms) ⁴			Leakage inductance ⁵	Turns ratio ⁶		Ipk ³	
number ¹	±10% (μΗ)	min (µH)	pri	bias	sec	max (µH)	pri : sec	pri : bias	(A)	Output ⁷
C1590-AL_	310	279	1.02	2.01	0.066	4.75	1:0.19	1:0.70	0.3	3.3 V, 0.91 A
C1591-AL_	310	279	1.02	2.01	0.118	4.50	1:0.28	1:0.70	0.3	5.0 V, 0.6 A
C1592-AL_	310	279	1.04	2.01	0.700	4.25	1:0.70	1 : 0.70	0.3	12 V, 0.25 A

1. When ordering, please specify packaging code:

C1592-ALD

- Packaging: D = 13" machine-ready reel. EIA-481 embossed plastic tape (350 parts per full reel).
 - B = Less than full reel. In tape, but not machine ready. To have a leader and trailer added (\$25 charge), use code letter D instead.
- 2. Inductance is for the primary, measured at 10 kHz, 0.1 Vrms, 0 Adc.
- 3. Ipk is peak primary current drawn at minimum input voltage.
- 4. DCR for the secondary is per winding.
- 5. Leakage inductance measured between pins 3 and 4 with all other pins shorted.
- 6. Turns ratio is with the secondary windings connected in parallel.
- 7. Output of the secondary is with the windings connected in parallel. Bias winding output is 12 V, 20 mA.
- 8. Electrical specifications at 25°C.
- Refer to Doc 362 "Soldering Surface Mount Components" before soldering.

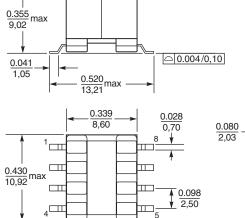
Pri

4

2

Bias

1



Dot above pin1



Recommended Land Pattern 0.050 0 370 9.40 0.098 2 50

> Specifications subject to change without notice. Please check our website for latest information.

Document 459 Revised 10/29/08

Secondary windings to be connected

in parallel on the PC board

Sec

7

8

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